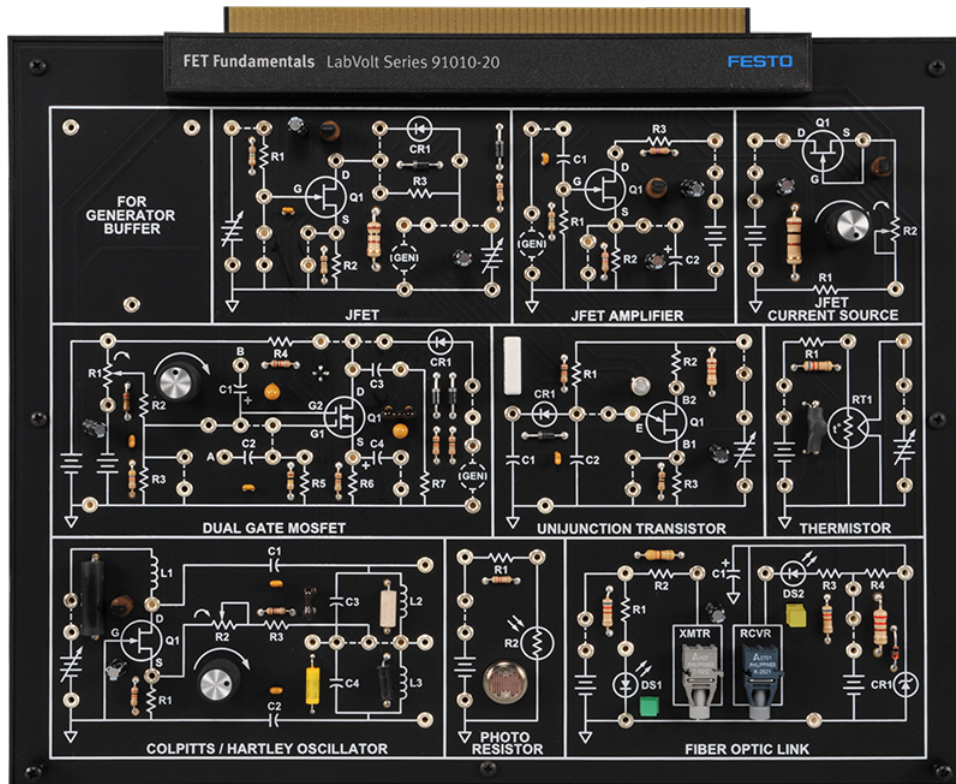


FET Fundamentals FACET Board 580985 (91010-20)

FESTO

LabVolt Series

Datasheet



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General Description

The FET Fundamentals module contains nine circuit blocks which enable students to perform practical exercises that demonstrate principles of JFET, MOSFET, and UJT:

- JFET
- JFET Amplifier
- JFET Current Source
- Dual Gate MOSFET
- Unijunction Transistor
- Thermistor
- Colpitts/Hartley Oscillator
- Photo Resistor
- Fiber Optic Link

Topic Coverage

- Component Location and Identification
- Oscillators Operation: Unijunction, Hartley, Colpitts
- JFET: Operating Characteristics, Effect of Gate Bias on Pinch-Off, Dynamic Characteristics, DC Amplifier Operation, Voltage Gain, DC Current Source Operation and Power/Load Voltage Variation
- MOSFET: Zero Bias Characteristic, Modes of Operation, Voltage Amplifier, Dual Gate MOSFET Mixer
- UJT: Operating Characteristics, Waveform Generation
- Thermistor and Photoresistor Operation
- Fiber Optic Light Transfer
- Troubleshooting Basics and Troubleshooting FET Circuits
- Understanding Specification Sheets: FET, Unijunction Transistor, Transducer

Reflecting the commitment of Festo Didactic to high quality standards in product, design, development, production, installation, and service, our manufacturing and distribution facility has received the ISO 9001 certification.

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Festo Didactic SE

Rechbergstrasse 3
73770 Denkendorf
Germany

P. +49(0)711/3467-0
F. +49(0)711/347-54-88500

Festo Didactic Inc.

607 Industrial Way West
Eatontown, NJ 07724
United States

P. +1-732-938-2000
F. +1-732-774-8573

Festo Didactic Ltée/Ltd

675 rue du Carbone
Québec QC G2N 2K7
Canada

P. +1-418-849-1000
F. +1-418-849-1666

www.labvolt.com

www.festo-didactic.com