



Digital Logic Fundamentals 5.00.09 to 5.00.10

Unit 3: EXCLUSIVE OR/NOR Gate Functions: Procedure: Question 18: Revised Feedback

Directions:

TechLab 6.0 & higher:

Download the file F50140030E.zip to your desktop. Then, extract F50140030E.tbk to the Tlssystem\unit\F50140030E directory on your TechLab server, overwriting the existing file.

Unit 4: DC operation of a NOT and an OT-TIE: Procedure: Moved highlight

Directions:

TechLab 6.0 & higher:

Download the file F50140040E.zip to your desktop. Then, extract F50140040E.tbk to the Tlssystem\unit\F50140040E directory on your TechLab server, overwriting the existing file.

Unit 6: Static JK Flip-Flop Operation: Procedure: Question 13: Updated feedback

Directions:

TechLab 6.0 & higher:

Download the file F50140060E.zip to your desktop. Then, extract F50140060E.tbk to the Tlssystem\unit\F50140060E directory on your TechLab server, overwriting the existing file.

Unit 8: Trigger Levels of TTL and CMOS Gates: Discussion: Revised question

Directions:

TechLab 6.0 & higher:

Download the file F50140080E.zip to your desktop. Then, extract F50140080E.tbk to the Tlssystem\unit\F50140080E directory on your TechLab server, overwriting the existing file.

Course Title: Revised version number
Revised answer

Directions:

TechLab 6.0 & higher:

Download the file F501400E.zip to your desktop. Then extract the entire contents to the tldata\gp2 directory on your TechLab server, overwriting the existing files.